

Fig. 1

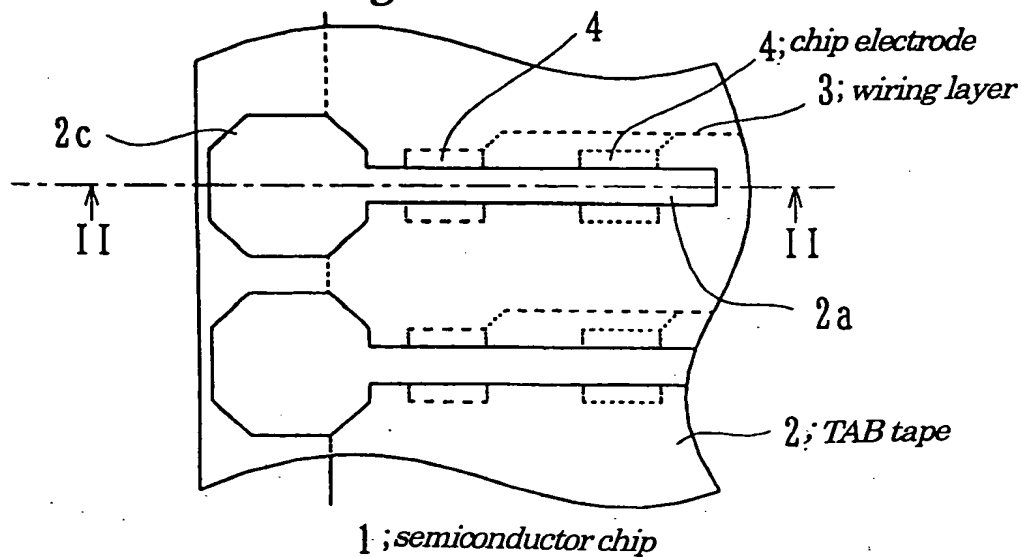


Fig. 2(a)

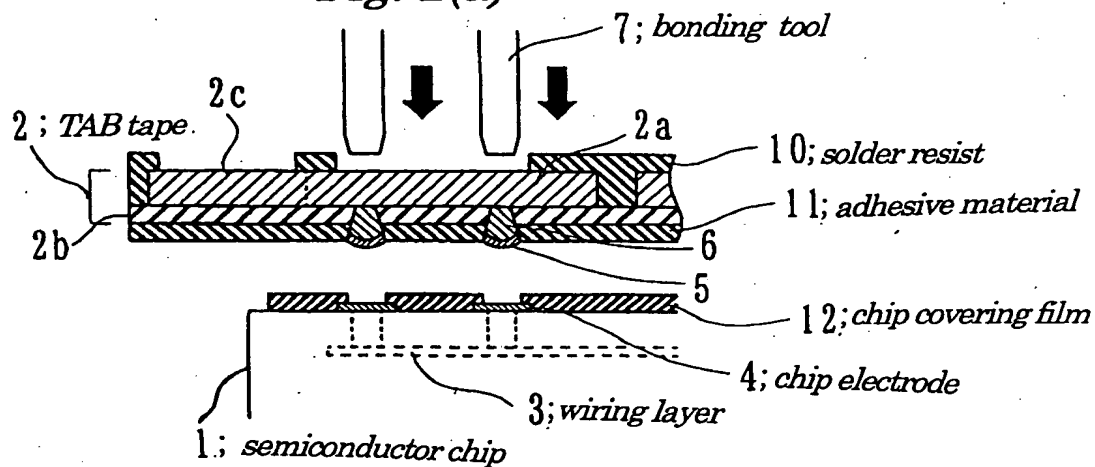
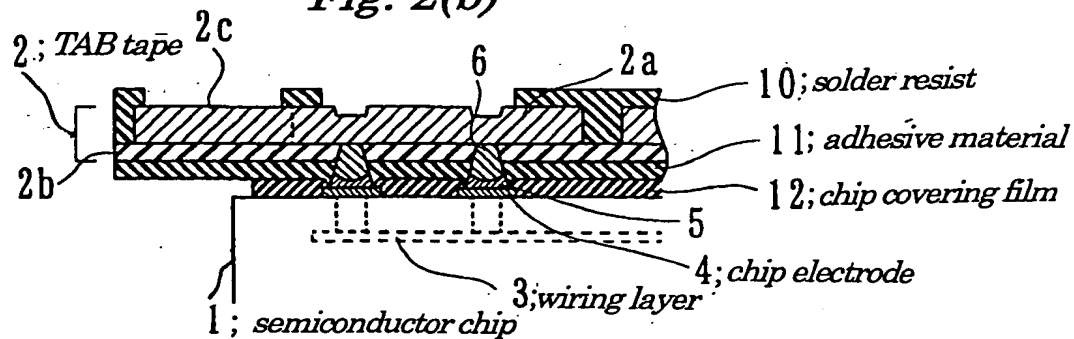


Fig. 2(b)



1; semiconductor chip **Fig. 3**

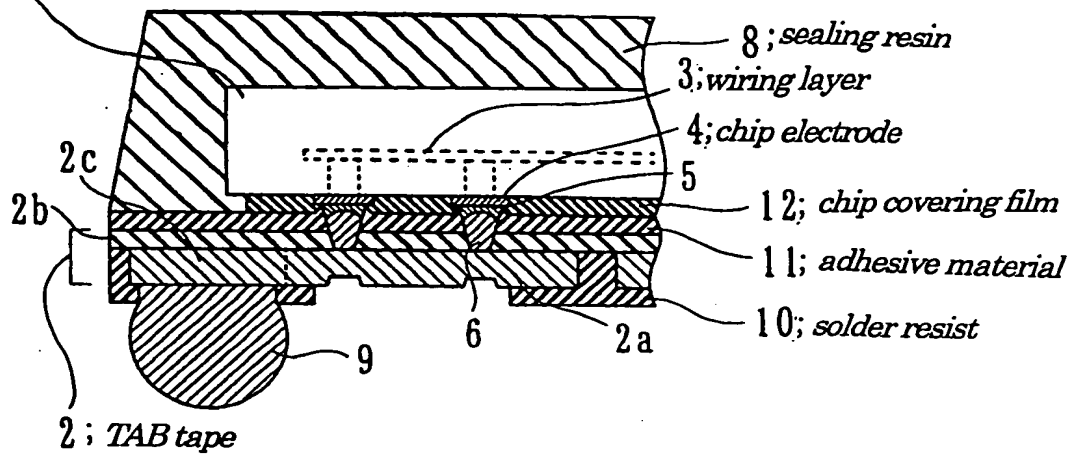


Fig. 4

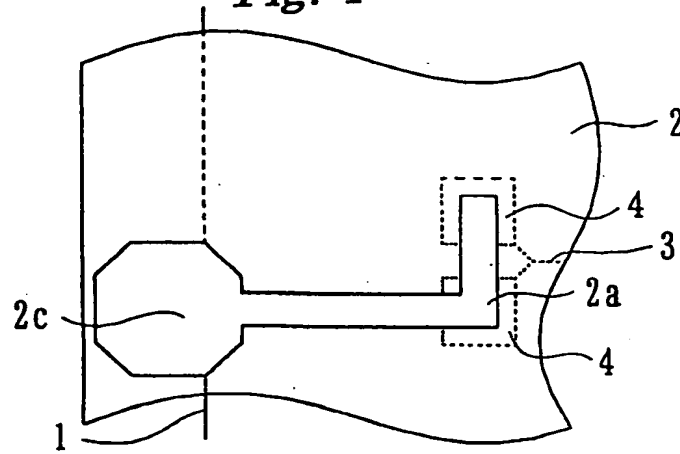
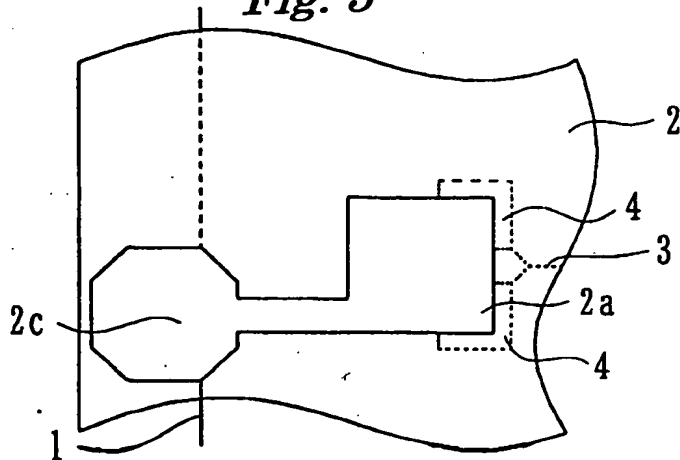


Fig. 5



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Fig. 6

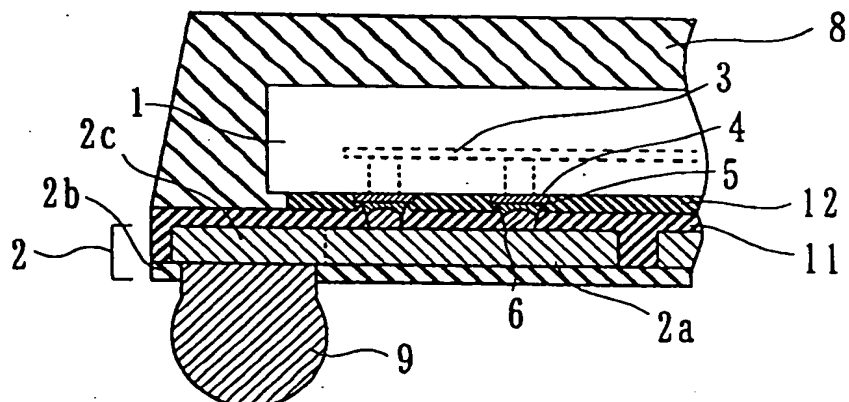


Fig. 7 (PRIOR ART)

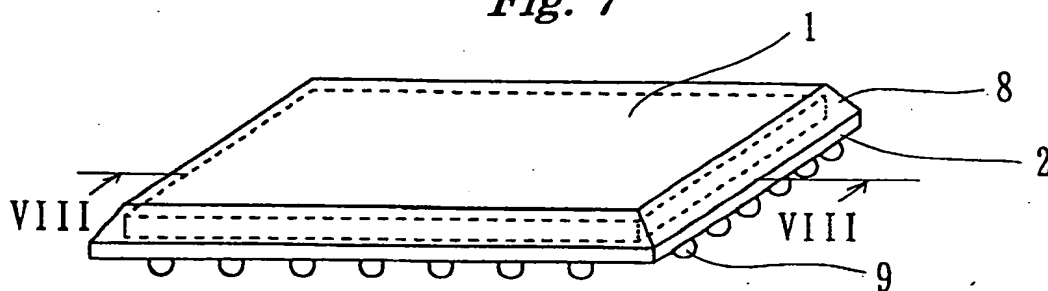


Fig. 8 (PRIOR ART)

